

Electronic Acknowledgement Receipt

EFS ID:	1143823
Application Number:	10667491
Confirmation Number:	1242
Title of Invention:	Reduced circuit trace roughness for improved signal performance
First Named Inventor:	Robert Sheffield
Correspondence Address:	Thomas E. Anderson Hunton & Williams LLP 1900 K Street, N.W. - Washington DC 20006-1109 US - -
Filer:	Thomas E. Anderson/Veronica Privott
Filer Authorized By:	Thomas E. Anderson
Attorney Docket Number:	57983.000131
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International Application Number:	

Payment information:

Submitted with Payment	yes
Payment was successfully received in RAM	\$500

RAM confirmation Number	172
Deposit Account	500206
The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows: Charge any Additional Fees required under 37 C.F.R. Section 1.16 and 1.17	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1		DOC000.PDF	1829216	yes	25
	Multipart Description				
	Doc Desc		Start	End	
	Transmittal letter		1	3	
	Appeal Brief Filed		4	25	

Warnings:

Information:

2	Fee Worksheet (PTO-875)	fee-info.pdf	8163	no	2
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Warnings:

Information:

Total Files Size (in bytes):	1837379
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.